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PATENT NUMBER and  
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U.S. UTILITY Patent Application

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APPL NUM 10092645	FILING DATE 03/06/2002	CLASS 711	SUBCLASS 122	GAU 2185	EXAMINER VITAL
**APPLICANTS: Soltis Donald; 2185					
**CONTINUING DATA VERIFIED: none PMV					
**FOREIGN APPLICATIONS VERIFIED: none PMV					
PG-PUB DO NOT PUBLISH: <input type="checkbox"/>		RESCIND <input type="checkbox"/>			
Foreign priority claimed <input type="checkbox"/> yes <input checked="" type="checkbox"/> no		35 USC 119 conditions met <input type="checkbox"/> yes <input checked="" type="checkbox"/> no		ATTORNEY DOCKET NO 10016693-1	
Verified and Acknowledged Examiner's initials		PMV			
TITLE: System and method for dynamic processor core and cache partitioning on large-scale multithreaded, multiprocessor integrated circuits					
U.S. DEPT. OF COMM./PAT. & TM-PTO-436L (Rev. 12-94)					

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Total Claims	Print Claim for O.G.
ISSUE FEE		DRAWING	
Amount Due	Date Paid	Sheets Drwg.	Figs. Drwg. Print Fig.
<input type="checkbox"/> TERMINAL DISCLAIMER		Primary Examiner PREPARED FOR ISSUE Application Examiner	
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